

**EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to the applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
3. Authorization for this examiner's amendment was given in a telephone interview with Mr. Anthony Moosey on 11/10/2008.
4. Please replace claims 9 and 12 with the following, respectively:
  9. A processor comprising:
    - (a) a plurality of cryptographic acceleration units, each of said plurality of cryptographic acceleration units comprising:
      - (1) a command first-in-first-out (FIFO) queue to store ~~one or more~~ commands, the command comprising a first command associated with a first context;

(2) a plurality of cores;

(3) a like plurality of same-context-command FIFOs configured to store commands of a same context, the same-context-command FIFOs comprising a first same-context-command FIFO coupled to the first core and a second same-context-command FIFO coupled to the second core queues, each of said plurality of same context command FIFO queues coupled to a corresponding one of said first plurality of cores; and

(4) a scheduler coupled to said command FIFO queue and to each of said first plurality of cores in said core group, configured to receive the first command from the command FIFO and to:

if a first core is idle, determine whether a second core is idle;

if the second core is not idle and the second core is processing a second command associated with a second context, determine whether the second context is the same as the first context; and

if the second context is the same as the first context, store the first command in the second same-context-command FIFO.

12. A processor comprising:

A cryptographic acceleration unit comprising:

(1) a command first-in-first-out (FIFO) queue to store one or more commands, the command comprising a first command associated with a first context;

(2) a core group comprising a plurality of cores;

(3) a like plurality of same-context-command FIFOs configured to store commands of a same context, the same-context-command FIFOs comprising a first same-context-command FIFO coupled to the first core and a second same-context-command FIFO coupled to the second core queues, each of said plurality of same context command FIFO queues coupled to a corresponding one of said first plurality of cores; and

(4) a scheduler coupled to said command FIFO queue and to each of said first plurality of cores in said core group, configured to receive the first command from the command FIFO and to:

if a first core is idle, determine whether a second core is idle;

if the second core is not idle and the second core is processing a second command associated with a second context, determine whether the second context is the same as the first context; and

if the second context is the same as the first context, store the first command in the second same-context-command FIFO.

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to MENGYAO ZHE whose telephone number is (571)272-6946. The examiner can normally be reached on Monday Through Friday, 7:30 - 5:00 EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng-Ai An can be reached on 571-272-3756. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Meng-Ai An/  
Supervisory Patent Examiner, Art Unit 2195